晶采光電科技股份有限公司
AMPIRE CO., LTD.

Specifications for LCD module

Customer	
Customer part no.	
Ampire part no.	AM-640480V2TZQW-TA1H
Approved by	
Date	

☐ Preliminary Specification
☑ Formal Specification

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*This specification is subject to change without notice.

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2022/11/02	---	New Release	Tank

1. Features

5.7 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This TFT LCD has a 5.7 (4:3) inch diagonally measured active display area with 640x480 (640 horizontal by 480 vertical pixels) resolution. This module is composed of a 5.7" TFT-LCD panel and backlight unit.

(1) Construction: a-Si TFT-LCD with driving system, White LED Backlight.

(2) LCD type : IPS

(3) Number of the Colors : 262K colors (R,G,B 6bit digital each)

(4) Interface: 6 Bits color driver with LVDS interface.

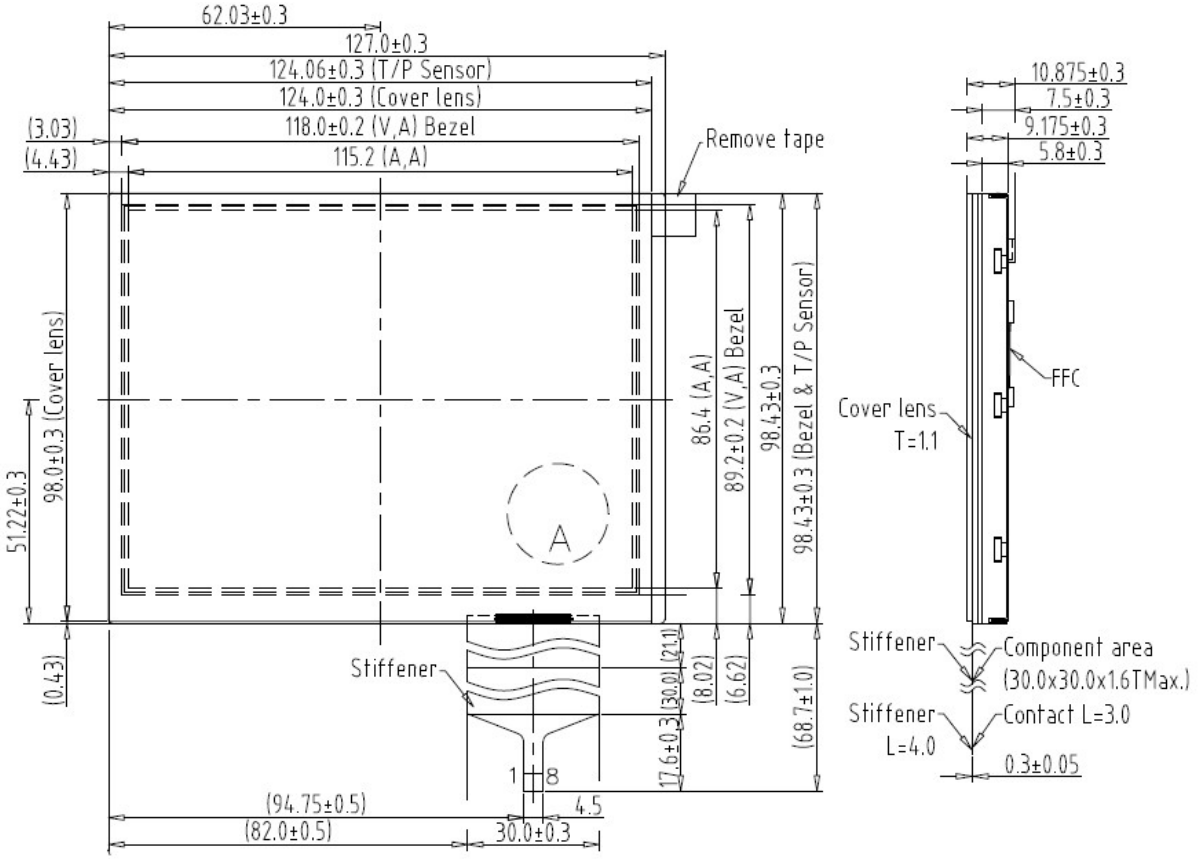
(5) LCD Power Supply Voltage: 3.3V single power input, built-in power supply circuit.

(6) Capacitive-type touch panel

i. Interface: USB
ii. Controller IC: EXC 80W32.
iii. Cover Lens T=1.1mm

2. Physical Specifications

Item	Specifications	unit
Display Size (diagonal)	5.7	inch
Resolution	640 (W) x RGB x 480 (H)	dot
Pixel Pitch	0.18 (W) x 0.18 (H)	mm
Color Configuration	R.G.B Vertical stripe	
Display Mode	Normally Black	



3. Absolute Maximum Ratings

3.1 Electrical Absolute max. ratings

ITEM	SYMBOL	MIN	MAX	UNIT	NOTE
Power Supply Voltage	V_{DD}	-0.5	+3.96	V	
Power Supply Voltage	V_{LED}	-0.5	+6	V	
Signal Input Voltage	DCLK , DE R0~R5 G0~G5 B0~B5	-0.5	V_{DD} + 0.5	V	

3.2 Environmental Absolute Maximum Ratings

Item	Operating		Storage		Remark
	Min.	Max.	Min.	Max.	
Temperature	-20	70	-30	80	Note(2),(3) ,(4),(5),(6),(7)
Humidity	Note(1)		Note(1)		
Corrosive Gas	Not Acceptable		Not Acceptable		

Note(1) Ambient temperature Temp. <= 60℃ : 90% RH max

Note(2) For storage condition Ta at -30℃ < 240h , at 80℃ < 240h

Note(3) For operating condition Ta at -20℃ < 100h , at 70℃ < 240h

Note(4) Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note(5) The response time will be slower at low temperature.

Note(6) Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at +25℃

Note(7) When LCM panel is operated over 60℃ (center of the panel surface temperature), the IAK of the LED back-light should be adjusted to 105 mA

Note(8) This is center of the panel surface temperature, not ambient temperature.

Note(9) At 25℃

4. Optical Characteristics

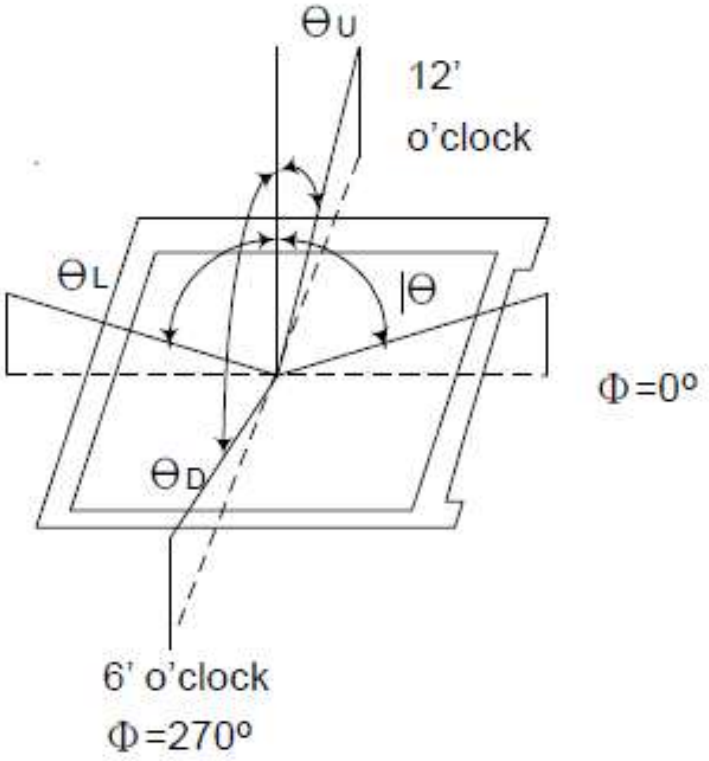
Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle	Hor.	θ U	CR≧10	75	85	--	deg.	(1),(4)
		θ D		75	85	--		
	Ver.	θ L		75	85	--		
		θ R		75	85	--		
Contrast ratio		CR	Θ=Φ=0°	800	1200	--	--	(1),(2)
Response Time		$T_R + T_F$	Θ=Φ=0°	--	30	45	msec	(1),(3)
NTSC		(%)		55	60	--	%	
Color chromaticity	Red	Rx	Θ=Φ=0°	Typ. -0.05	0.630	Typ. +0.05	--	(1),(4),(5)
		Ry			0.312			
	Green	Gx			0.278			
		Gy			0.583			
	Blue	Bx			0.147			
		By			0.115			
	White	Wx			**0.312**			
		Wy			**0.356**			
Luminance (IAK=TBDmA)		L	Θ=Φ=0°	**340**	**425**	--	cd/m^2	(1),(6)
Luminance Uniformity		ΔL	Θ=Φ=0°	70	-	-	%	(7)

Measuring Condition

Ta=25°C. To be measured on the center area of panel after 10 minutes operation. LED Back-light IAK=140 mA.

- Measuring surrounding：Dark room
- Ambient temperature：25±2℃
- 15min. Warm-up time.

Note(1) Definition of Viewing Angle

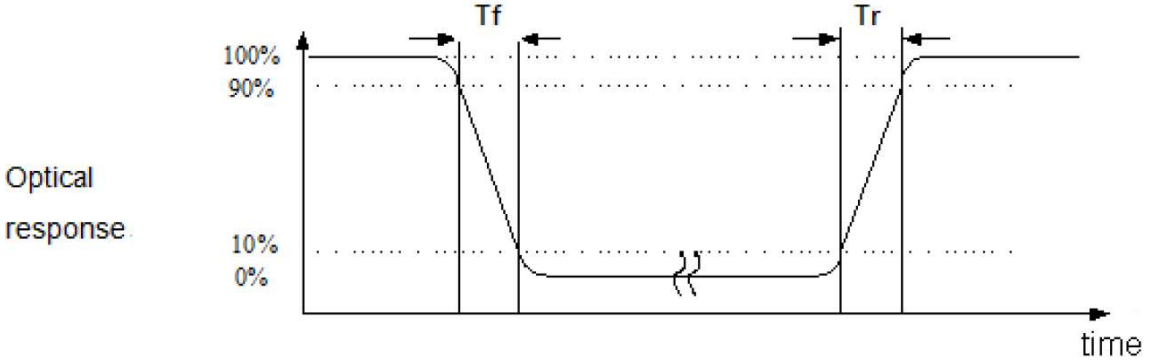


Note(2) Definition of Contrast Ratio (CR) :

Contrast ratio is calculated with the following formula.

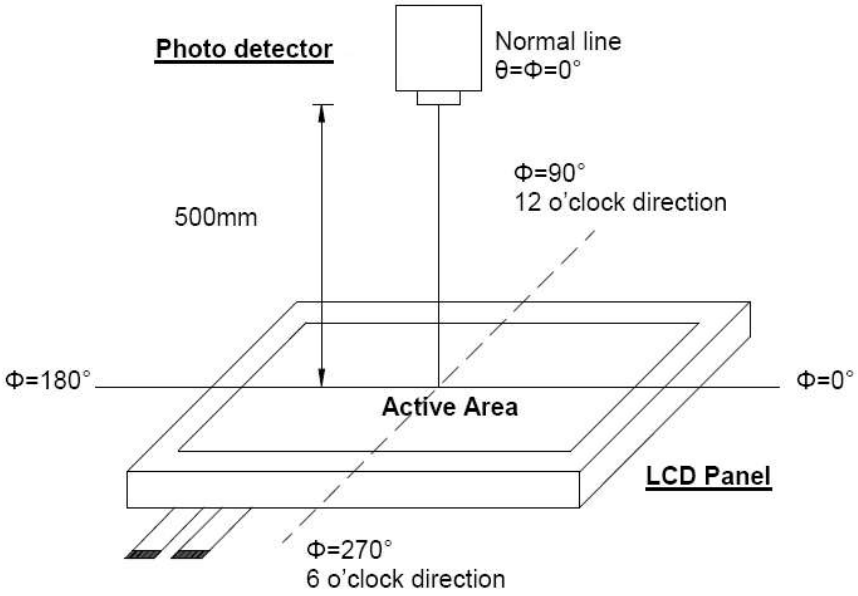
$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at “White” state}}{\text{Photo detector Output when LCD is at “Black” state}}$$

Note(3) Definition of Response Time : Sum of TR and TF



Note(4) Definition of optical measurement setup

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° / Height: 500mm.)

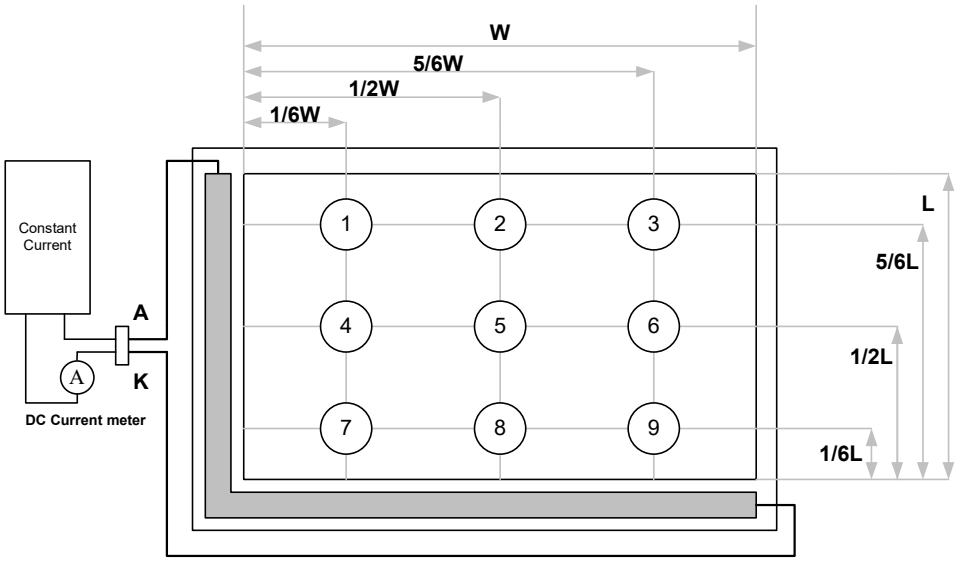


Note(5) Definition of color chromaticity (CIE1931)

Color coordinated measured at center point of LCD.

All input terminals LCD panel must be ground when measuring the center area of the panel.

Note(6) Luminance is measured at point 5 of the display.



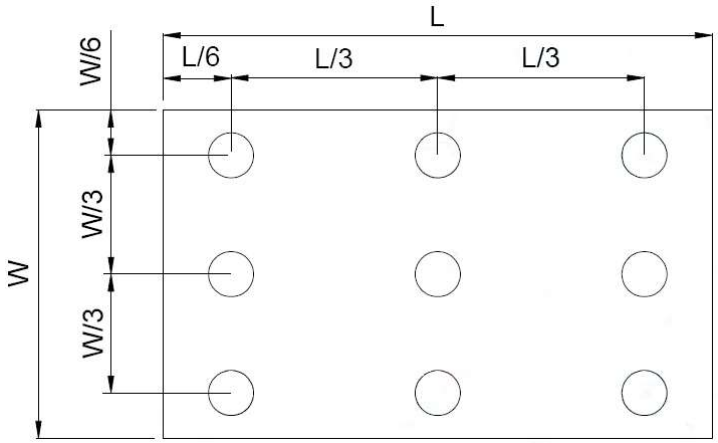
Note(7) Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to bellow figure).

Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{\text{Bmin}}{\text{Bmax}}$$

L ----- Active area length W ----- Active area width



Bmax: The measured maximum luminance of all measurement position.

Bmin: The measured minimum luminance of all measurement position.

5. Electrical Characteristics

5.1 DC Characteristics

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Power Voltage For LCD	V_{DD}	3.0	3.3	3.6	V	
Power Voltage For VLED	V_{LED}	--	5.0	--	V	
Logic Input Voltage	VIH	V_{DD}*0.7	--	V_{DD}	V	
	VIL	0	--	V_{DD}*0.3	V	
ADJ Input Voltage	VIH	3.0	--	5.0	V	
	VIL	GND	--	0.3	V	

5.2 Electrical Characteristic Of LED Backlight

Item	Symbol	Min.	Typ.	Max.	Unit	Note
LED Forward Voltage	VAK	7.8	8.2	10.5	V	IAK=140mA, Ta=25℃
LED Forward Current	IAK	--	140	--	mA	Ta=25℃
LED life time			50k	-	Hrs.	IAK=140mA, Ta=25℃

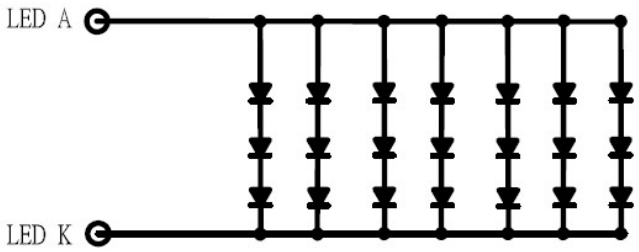
Note(1) Ta means ambient temperature of TFT-LCD module.

Note(2) If the module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

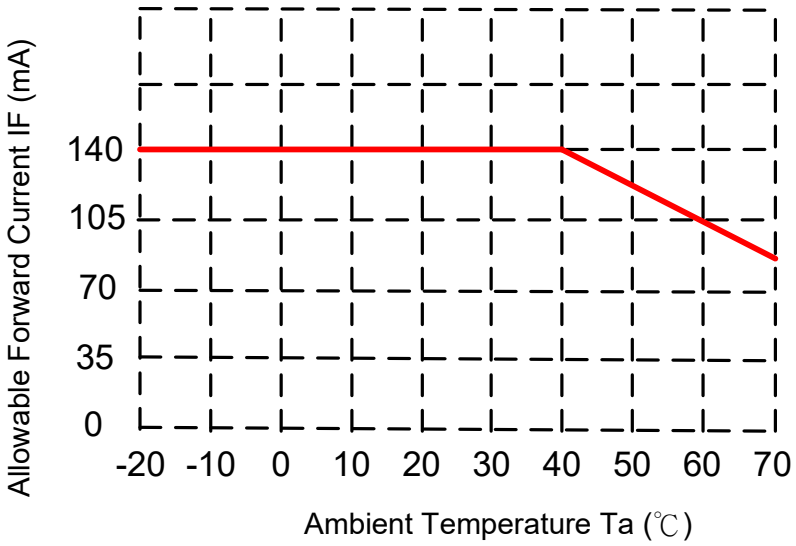
Note(3) The constant current source is needed for LED back-light driving.

Note(4) Operating life means brightness goes down to 50% minimum brightness. LED life time is estimated data. Ta=25℃

Note(5) The structure of LED B/L shows as below.



Note(6) When LCM is operated over 60℃ ambient temperature, the IAK of the LED backlight should be adjusted to 105 mA max



6. Interface Pin Assignment

LVDS J2:

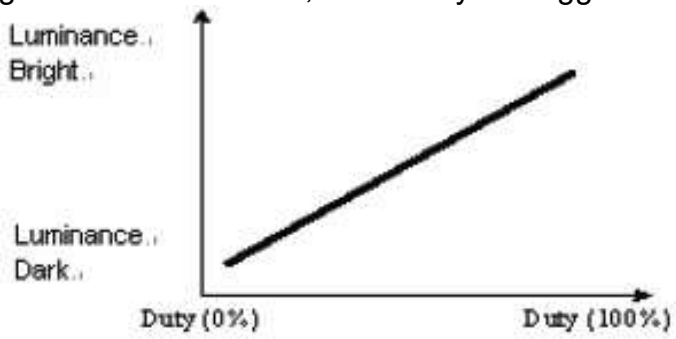
Pin no	Symbol	Function
1	V_{DD}	POWER SUPPLY:3.3V
2	V_{DD}	POWER SUPPLY:3.3V
3	Gnd	Power Ground
4	Gnd	Power Ground
5	IN0-	Transmission Data of Pixels
6	IN0+	Transmission Data of Pixels
7	Gnd	Power Ground
8	IN1-	Transmission Data of Pixels 1
9	IN1+	Transmission Data of Pixels 1
10	Gnd	Power Ground
11	IN2-	Transmission Data of Pixels 2
12	IN2+	Transmission Data of Pixels 2
13	Gnd	Power Ground
14	CLK-	Sampling Clock
15	CLK+	Sampling Clock
16	Gnd	Power Ground
17	NC	No Connect
18	NC	No Connect
19	Gnd	Power Ground
20	Gnd	Power Ground

LED J3:

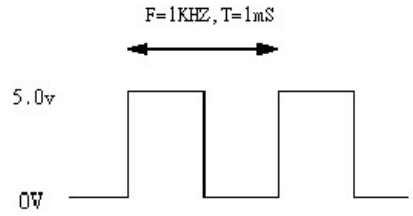
Pin no	Symbol	Function
1	GND	Power Ground
2	V_{LED}	Power Supply for LED 5V
3	V_{LED}	Power Supply for LED 5V
4	ADJ	Adjust for LED Brightness *Note

NOTE:

1. ADJ adjust brightness to control Pin, Pulse duty the bigger the brighter.



2. ADJ signal = 0 ~ 5.0V , operation frequency : 300Hz~10KHz



3. VSS Pin must ground contact, cannot be floating.

7. Interface Timing
7. AC Timing characteristic of the LVDS

Switching Characteristics

over recommended operating conditions (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP(1)	MAX	UNIT
t_{su}	Setup time, D0–D20 to CLKOUT↓	C_L = 8 pF, See Figure 5	5			ns
t_h	Data hold time, CLKOUT↓ to D0–D20		5			ns
$t_{(RSKM)}$	Receiver input skew margin(2) (see Figure 7)	t_c = 15.38 ns (±0.2%), |Input clock jitter| < 50 ps,(3)	550	700		ps
t_d	Delay time, CLKIN↑ to CLKOUT↓ (see Figure 7)	V_{CC} = 3.3 V, t_c = 15.38 ns (±0.2%), T_A = 25°C	3	5	7	ns
t_{en}	Enable time, $\overline{SHTDN}$ to phase lock	See Figure 7		1		ms
t_{dis}	Disable time, $\overline{SHTDN}$ to off state	See Figure 8		400		ns
t_t	Transition time, output (10% to 90% t_r or t_f) (data only)	C_L = 8 pF		3		ns
t_t	Transition time, output (10% to 90% t_r or t_f) (clock only)	C_L = 8 pF		1.5		ns
t_w	Pulse duration, output clock			0.50 t_c		ns

(1) All typical values are at V_{CC} = 3.3 V, T_A = 25°C.
(2) The parameter $t_{(RSKM)}$ is the timing margin available to allocate to the transmitter and interconnection skews and clock jitter. The value of this parameter at clock periods other than 15.38 ns can be calculated from t_{RSKM} = tc/14 – 550 ps.
(3) |Input clock jitter| is the magnitude of the change in input clock period.

PARAMETER MEASUREMENT INFORMATION (continued)

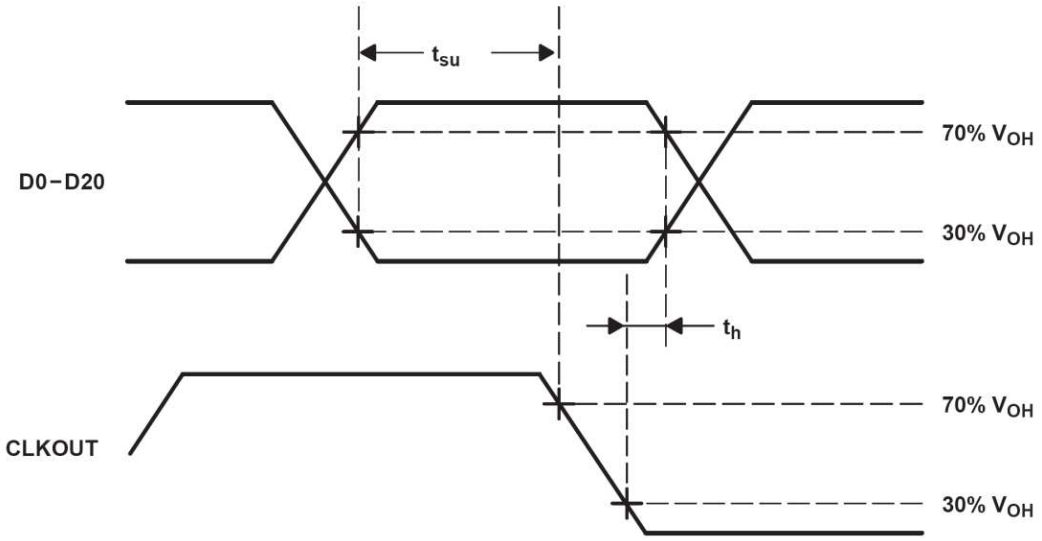


Figure 5. Setup and Hold Time Waveforms

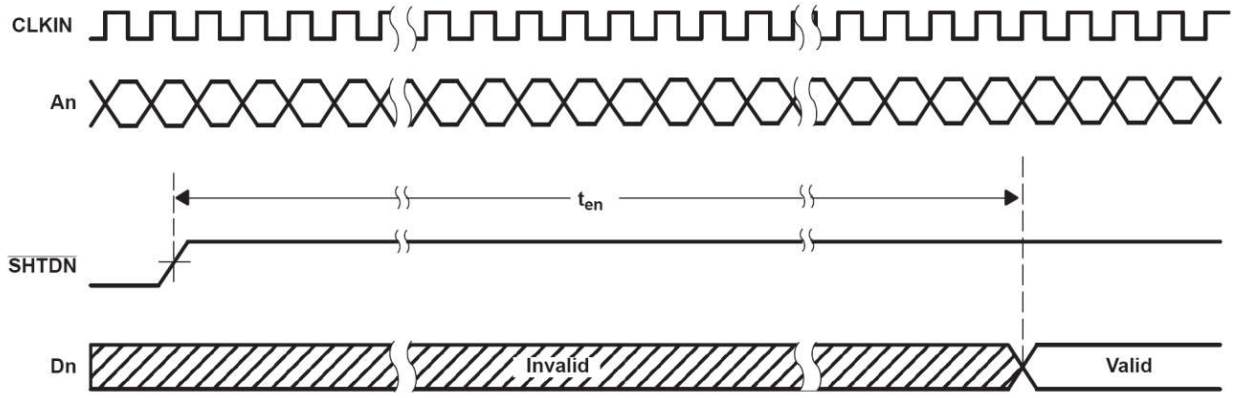


Figure 7. Enable Time Waveforms

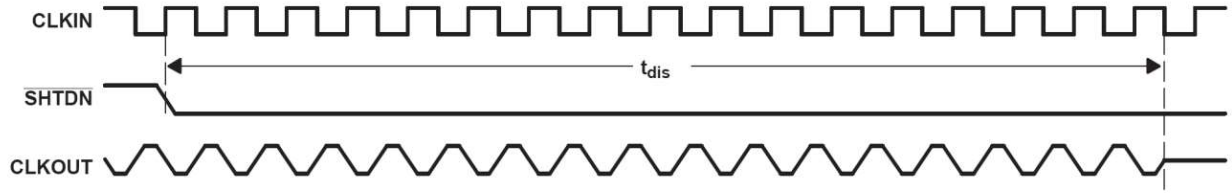


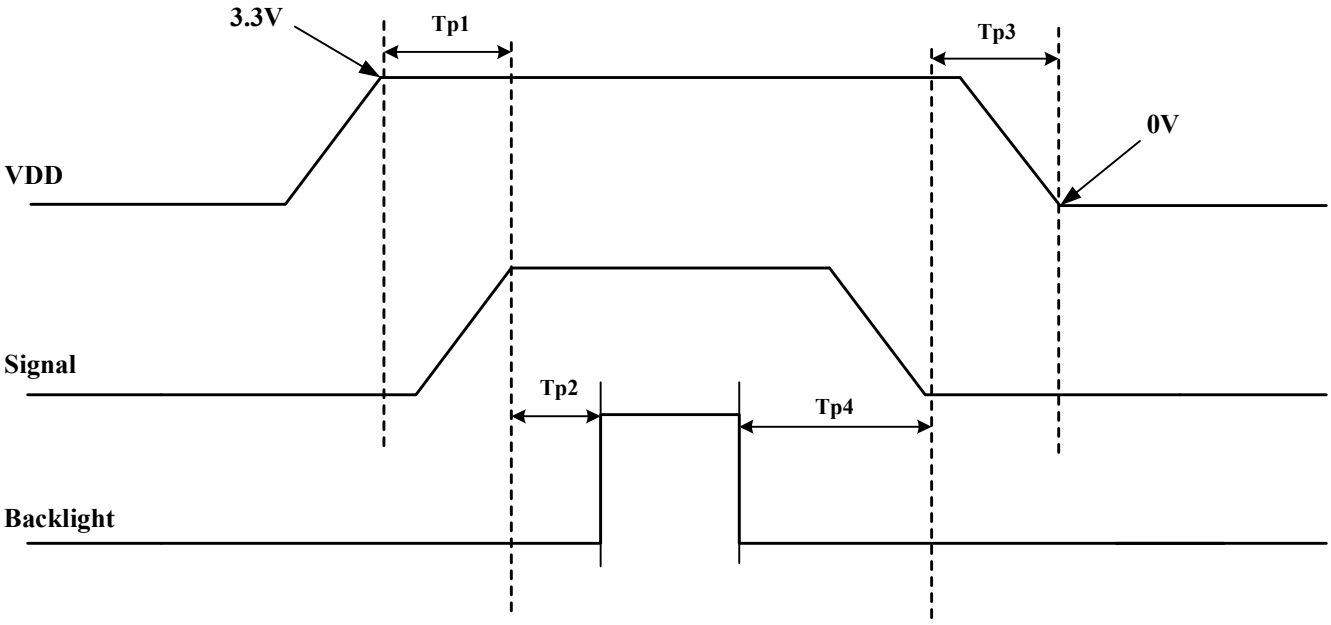
Figure 8. Disable Time Waveforms

Timing for RGB Interface

VDD=3.3V, VSS=0V, Ta=25℃

Parameter	Symbol	Min	Typ	Max	Unit	Remark
DCLK frequency	fclk	--	24	50	MHz	
Horizontal display area	thd		640		DCLK	
One Horizontal Line	th	--	760	--	DCLK	
HSD pulse width	thpw	1	48	255	DCLK	
HSD Back Porch(blanking)	thb		88		DCLK	
HSD Front Porch	thfp	1	32	255	DCLK	
DE Mode Blanking	th-thd	85	120	512	DCLK	
Vertical display area	tvd		480		TH	
VSD period time	tv	513	525	767	TH	
VSD pulse width	tvpw	3	3	255		
VSD Back Porch(blanking)	tvb		32		TH	
VSD Front Porch	tvfp	1	13	255	TH	
DE Mode Blanking	tv-tvd	4	45	255	TH	

8. Power On/Off Sequence



Item	Symbol	Value			Units	Remark
		Min.	Typ.	Max.		
VDD on to signal starting	Tp1	5	-	50	ms	
Signal starting to backlight on	Tp2	150	-	-	ms	
Signal off to VDD off	Tp3	5	-	50	ms	
Backlight off to signal off	Tp4	150	-	-	ms	

9. Displayed Color and Input Data

Data Signal

COLOR		INPUT DATA																							
		R DATA								G DATA								B DATA							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB							LSB	MSB							LSB	MSB							LSB
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

10. Projected capacitive-type touch panel

10.1 Basic Characteristic

ITEM	SPECIFICATION
Type	Projective Capacitive Touch Panel
Activation	5-fingers or Signal-finger
X/Y Position Reporting	Absolute Position
Touch Force	No contact pressure required
Calibration	No need for calibration
Report Rate	Approx. 100 points/sec
Interface	USB
Control IC	EXC80W32

Specify the normal operating condition

(GND=0V)

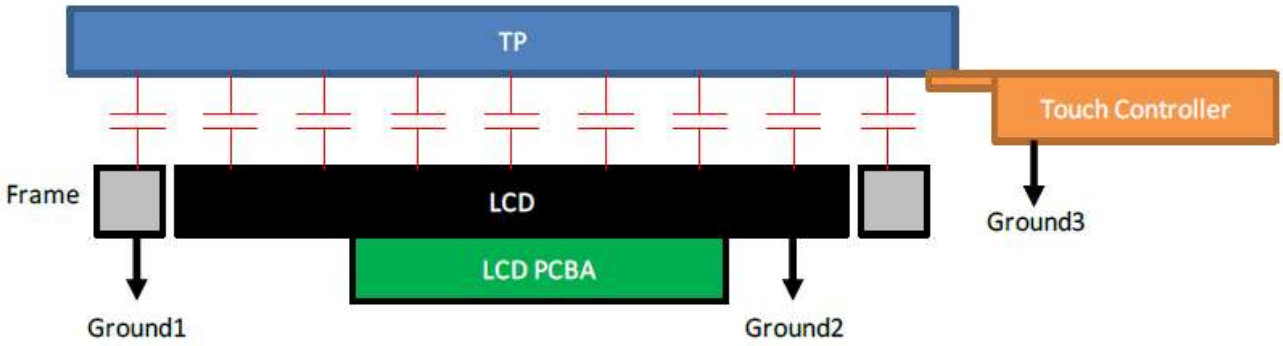
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage	VCC	4.75	5.0	5.25	V	
Power Consumption	Ivcc	-	T.B.D	-	mA	

8.2 Interface

Pin No.	Symbol	Function
1	VCC	USB Power 5V
2	D+	USB Data+
3	D+	USB Data+
4	GND	USB Power Ground
5	D-	USB Data-
6	D-	USB Data-
7	NC	No connection
8	NC	No connection

Grounding

TP needs to work in environment with stable stray capacitance. In order to minimize the variation in stray capacitance, all conductive mechanical parts must not be floating. Intermittent floating any conductive part around the touch sensor may cause significant stray capacitance change and abnormal touch function. It is recommended to keep all conductive parts having same electrical potential as the GND of the touch controller module.



GND1, GND2 and GND3 should be connected together to have the same ground

11. Reliability Test Conditions

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	(1),(2)
Low Temperature Storage	-30±3°C , t=240 hrs	(1),(2)
Storage Humidity Test	60 °C, Humidity 90%, 240 hrs	(1),(2)
Vibration Test (Packing)	Sweep frequency : 10 ~ 50 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	(2)

Note(1) Condensation of water is not permitted on the module.

Note(2) The module should be inspired after 1 hour storage in normal conditions (15~35℃, 45~65%RH).

Note(3) The module shouldn't be tested over one condition, and all the tests are independent.

Note(4) All reliability tests should be done without the protective film.

Definitions of life end point:

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of initial value.

12. Use Precautions

12.1 Handling precautions

(1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.

(2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.

(3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.

(4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

12.2 Installing precautions

(1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. 1MΩ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.

(2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.

(3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.

(4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

12.3 Storage precautions

(1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.

(2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.

(3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

12.4 Operating precautions

(1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.

(2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.

(3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.

(4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.

(5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.

(6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.

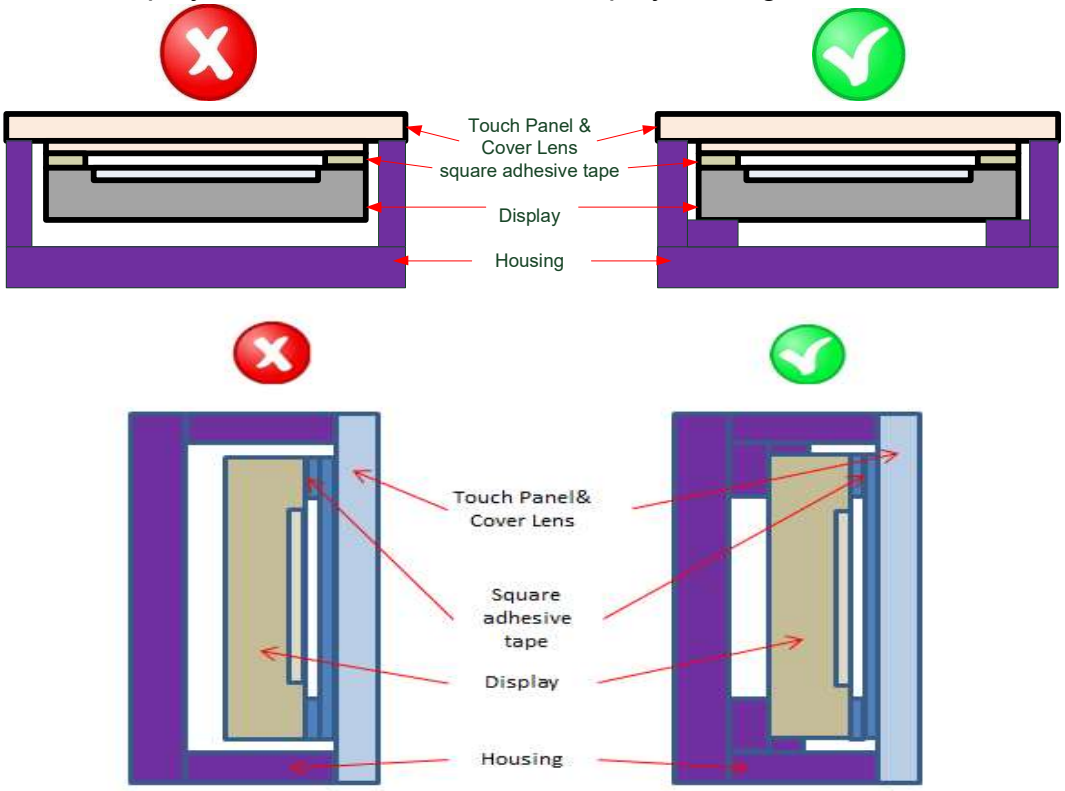
(7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.

(8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

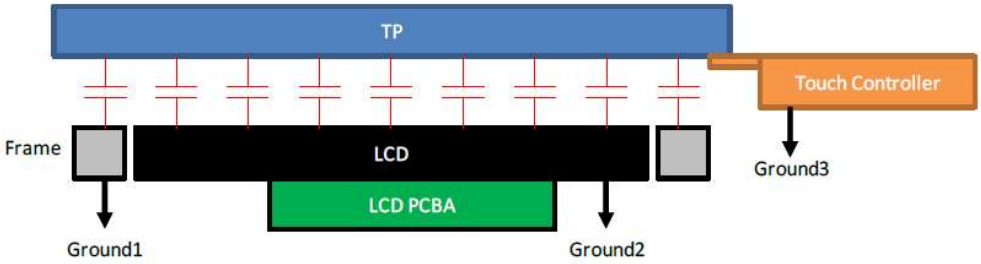
12.5 Mechanism

(1) Please mount LCD module by using mounting holes arranged in four corners tightly.

(2) The square adhesive tape which is between the touch panel and display can't provide well supporting in the long term and high ambient temperature condition. Whether upright or horizontal position the support holder which is in the back side of the display is needed. Do not let the display floating.



(3) TP needs to work in environment with stable stray capacitance. In order to minimize the variation in stray capacitance, all conductive mechanical parts must not be floating. Intermittent floating any conductive part around the touch sensor may cause significant stray capacitance change and abnormal touch function. It is recommended to keep all conductive parts having same electrical potential as the GND of the touch controller module.



GND1, GND2 and GND3 should be connected together to have the same ground

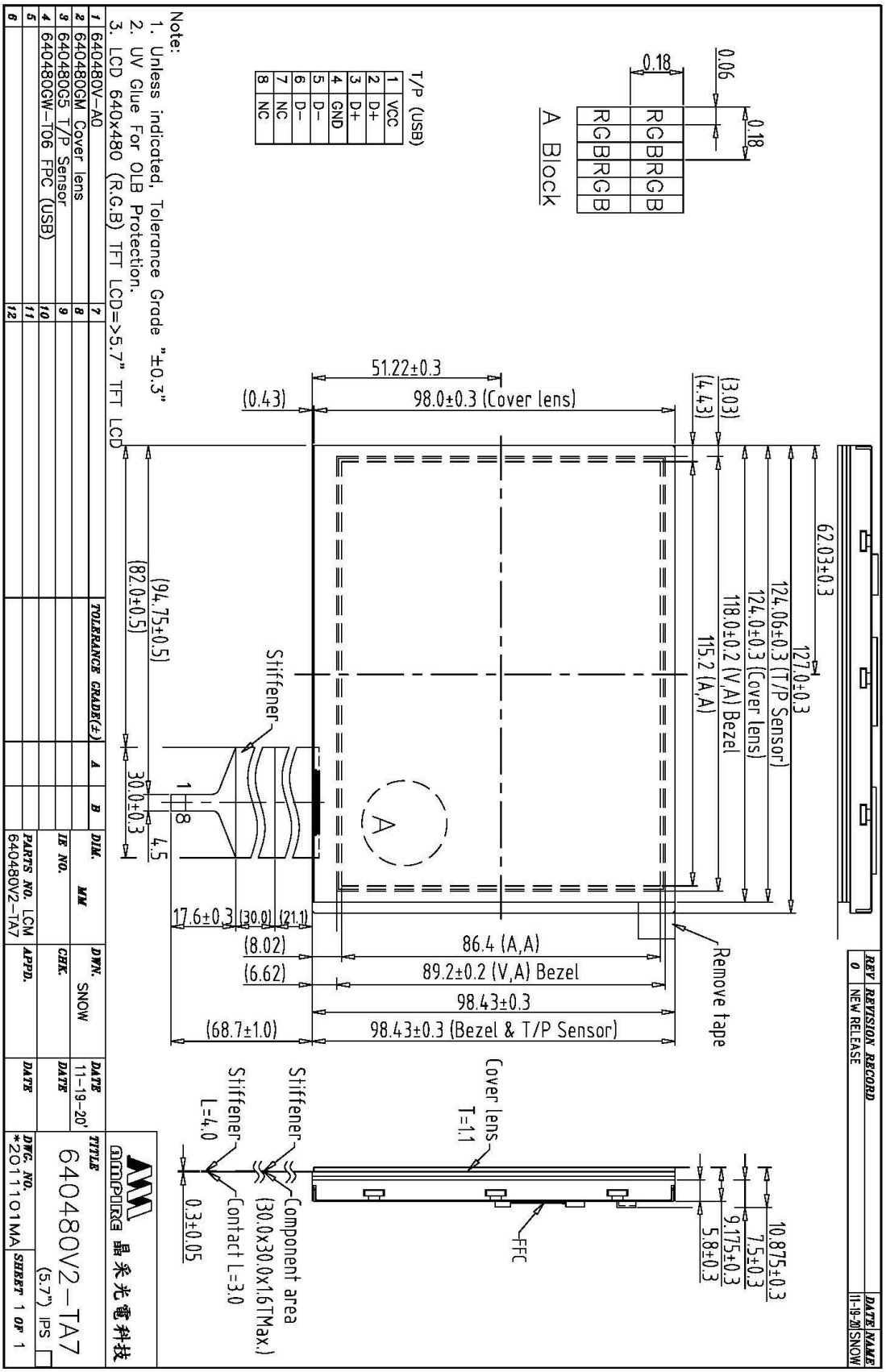
12.6 Other

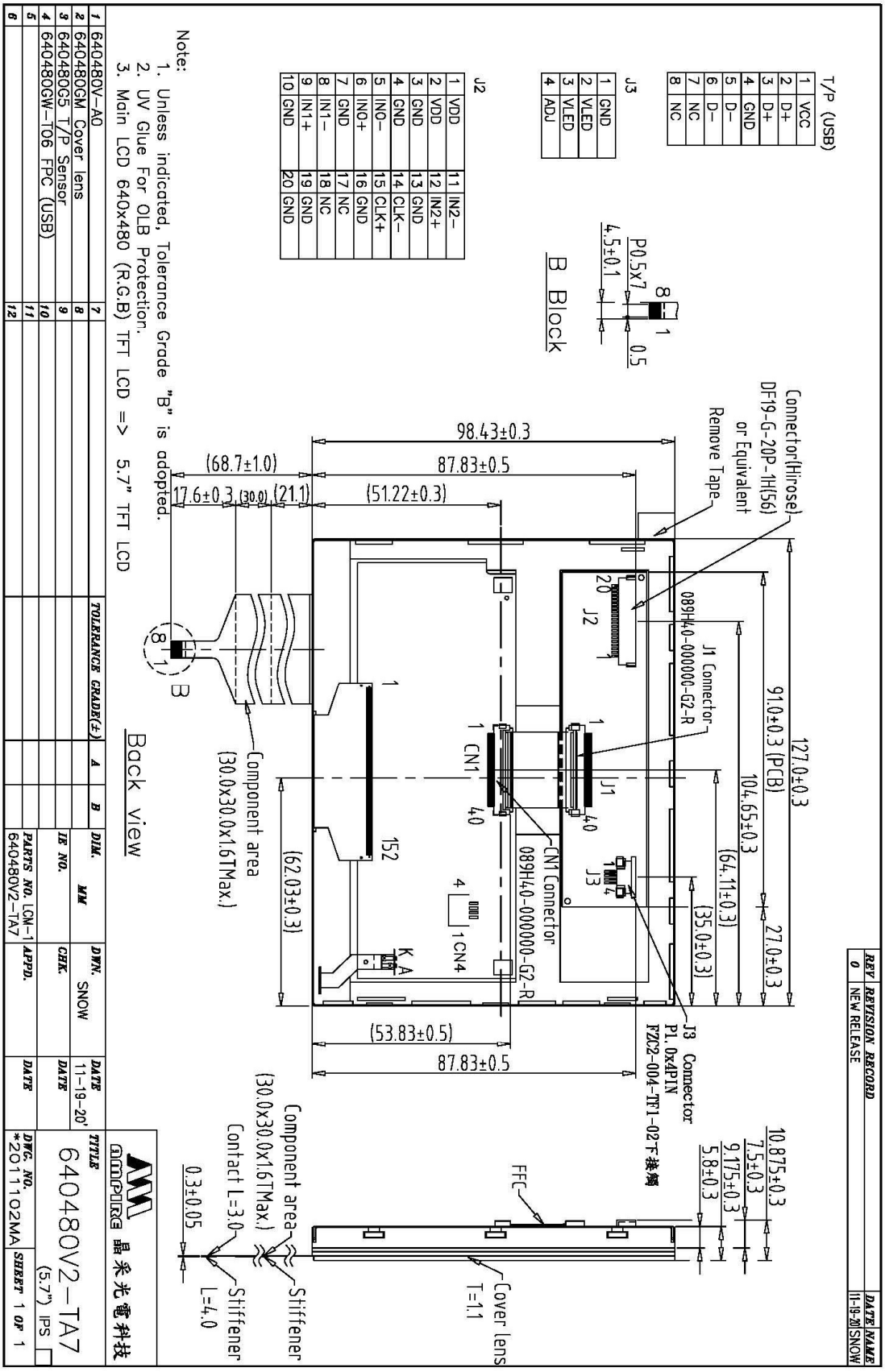
(1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.

(2) Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver.

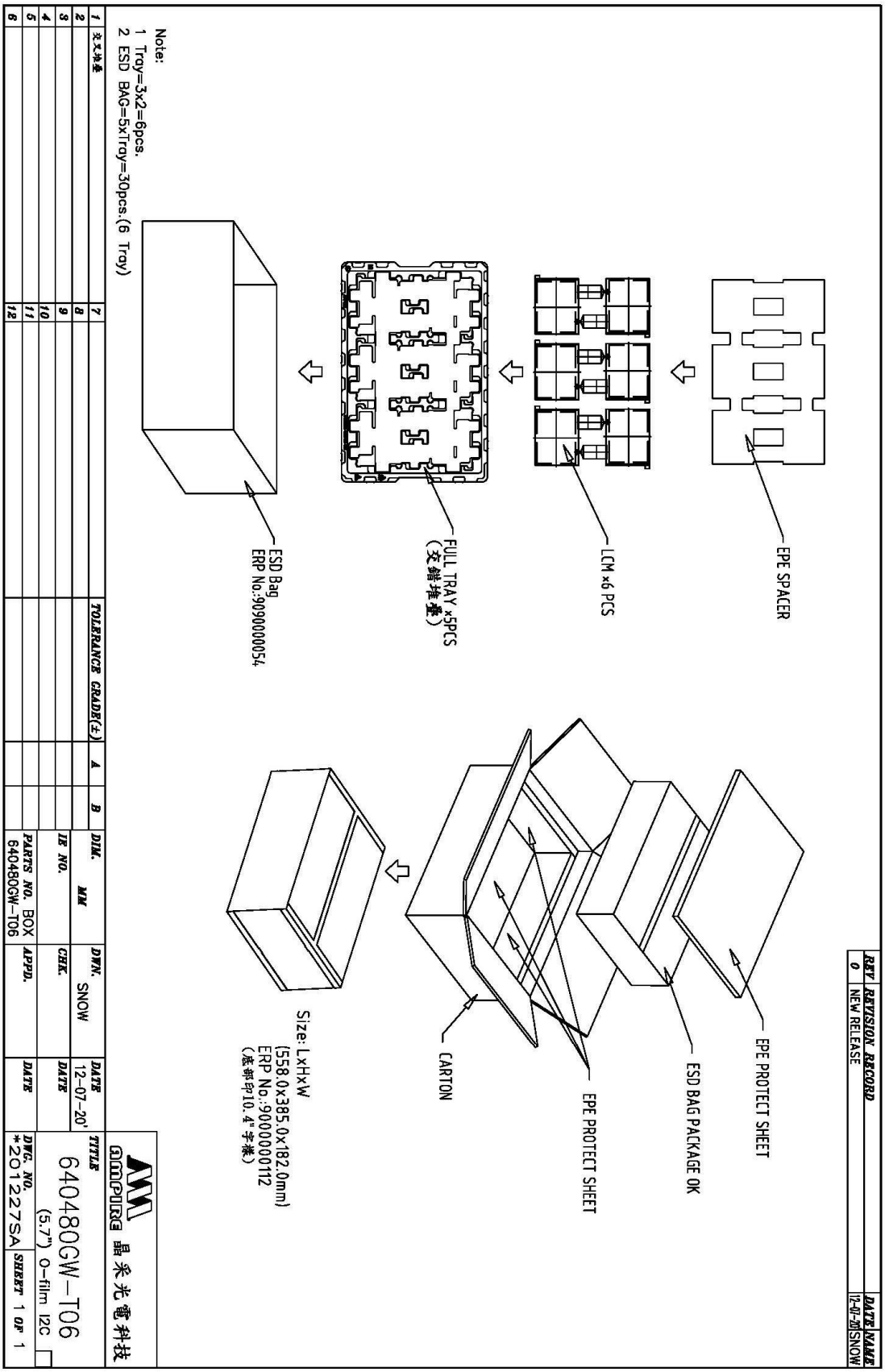
(3) AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.

13. Outline Dimension




T/P (USB)
1 VCC
2 D+
3 D+
4 GND
5 D−
6 D−
7 NC
8 NC
J3
1 GND
2 VLED
3 VLED
4 ADJ
J2
1 VDD 11 IN2−
2 VDD 12 IN2+
3 GND 13 GND
4 GND 14 CLK−
5 IN0− 15 CLK+
6 IN0+ 16 GND
7 GND 17 NC
8 IN1− 18 NC
9 IN1+ 19 GND
10 GND 20 GND
B Block
P0.5x7
4.5±0.1
0.5
Connector(Hirose)
DF19-G-20P-1H(56)
or Equivalent
Remove Tape
98.43±0.3
87.83±0.5
(68.7±1.0)
17.6±0.3 (30.0) (21.1)
(51.22±0.3)
089H40-000000-G2-R
J1 Connector
J2
J1
CN1
CN1 Connector
089H40-000000-G2-R
J3
CN4
127.0±0.3
91.0±0.3 (PCB)
104.65±0.3
(64.11±0.3)
(35.0±0.3)
27.0±0.3
J3 Connector
P1.0x4PIN
FZC2-004-TF1-02-下接觸
40
152
(62.03±0.3)
(53.83±0.5)
87.83±0.5
Component area
(30.0x30.0x1.6TMax.)
Back view
FFC
Cover lens
T=1.1
Stiffener
Component area
(30.0x30.0x1.6TMax.)
Contact L=3.0
Stiffener
L=4.0
0.3±0.05
10.875±0.3
7.5±0.3
9.175±0.3
5.8±0.3
Note:
1. Unless indicated, Tolerance Grade "B" is adopted.
2. UV Glue For OLB Protection.
3. Main LCD 640x480 (R.G.B) TFT LCD => 5.7" TFT LCD
REV. REVISION RECORD
0 NEW RELEASE
DATE NAME
11-19-20 SNOW
TOLERANCE GRADE(±) A B
DIM. MM
DWN. SNOW
CHK.
APPD.
DATE 11-19-20'
DATE
DATE
PARTS NO. LCM-1 640480V2-TA7
AMPIRE 晶采光電科技
TITLE
640480V2-TA7
(5.7") IPS
DWG. NO. *20111102MA
SHEET 1 OF 1
640480V-A0
640480GM Cover lens
640480C5 T/P Sensor
640480GW-T06 FPC (USB)

14. Package


REV
0
REVISION RECORD
NEW RELEASE
DATE
12-07-20
NAME
SNOW
EPE SPACER
LCM ×6 PCS
FULL TRAY ×5PCS
(交錯堆疊)
ESD Bag
ERP No:9090000054
EPE PROTECT SHEET
ESD BAG PACKAGE OK
EPE PROTECT SHEET
CARTON
Size: LxHxW
(558.0x385.0x182.0mm)
ERP No.:9000000112
(底部印10.4"字樣)
Note:
1 Tray=3x2=6pcs.
2 ESD BAG=5xTray=30pcs.(6 Tray)
TOLERANCE GRADE(±)
DIM.
MM
IE NO.
PARTS NO. 640480GW-T06
DWN.
SNOW
CHK.
APPD.
DATE
12-07-20'
DATE
DATE
AMPIRE 晶采光電科技
TITLE
640480GW-T06
(5.7") o-film I2C
DWG. NO.
*201227SA
SHEET 1 OF 1